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FORM PTO-892 (REV. 3-78)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	SERIAL NO.	GROUP ART UNIT	ATTACHMENT TO PAPER NUMBER	
NOTICE OF REFERENCES CITED			103,192	237		5
		APPLICANT(S)	Bergman			

## U.S. PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
A	4 342 082	7-27-82	Brown et al.	364	200	
B	4 641 275	2-3-87	Hatakeyama et al.	364	900	
C	4 454 578	6-12-84	Matsumoto et al.	364	200	
D	4 395 758	7-26-83	Helenius et al.	364	200	
E	4 384 324	5-17-83	Kim et al.	364	200	
F	4 332 008	5-25-82	Shima et al.	364	200	
G	4 136 383	1-23-79	Takesue	364	200	
H	3 916 383	10-28-75	Malcolm	364	200	
I	4 705 344	11-10-87	Hinton et al.	350	3.73	
J						
K						

## FOREIGN PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
L								
M								
N								
O								
P								
Q								

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R	Wu, W.H. et al., "Implementation of Optical Interconnections for VLSI", <u>IEEE Transactions on Electron Devices</u> , Vol. ED-34, No. 3, March. 1987, pp. 706 - 714
T	Bergman, L.A. et al. "Holographic Optical Interconnects For VLSI", <u>Optical Engineering</u> , Vol. 25, No. 10, October 1986, pp. 1109 - 1118.
U	

EXAMINER	DATE
<i>Phil. K.</i>	5-9-89

\* A copy of this reference is not being furnished with this office action.  
(See Manual of Patent Examining Procedure, section 707.05 (a).)